

04-15-04



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/666,845
Filing Date: September 19, 2003

Applicant: Takashi Fujise

Group Art Unit: 2817
Examiner: Unknown

Title: SEMICONDUCTOR INTEGRATED CIRCUIT

Attorney Docket: 9319S-000532

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Sir:

We acknowledge receipt of the Updated Filing Receipt (copy enclosed) for the above-identified patent application. However, the inventor's residence information is not correct. Please correct your records to show the city as "Shiojiri-shi". The correction is shown in red on the attached copy. Thank you for your assistance.

Respectfully submitted,

By: _____

G. Gregory Schivley
Reg. No. 27,382

Dated: April 14, 2004

Harness, Dickey & Pierce, P.L.C.
P.O. Box 828
Bloomfield Hills, MI 48303

CERTIFICATE OF MAILING

***I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on***

By _____

EV 406 076 754 US



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/666,845	09/19/2003	2817	900	9319S-000532	5	6	1

27572
 HARNESS, DICKEY & PIERCE, P.L.C.
 P.O. BOX 828
 BLOOMFIELD HILLS, MI 48303



CONFIRMATION NO. 5792

UPDATED FILING RECEIPT



OC000000012104804

Date Mailed: 03/15/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Shiojiri-shi
 Takashi Fujise, Shiojiri-shi, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-272471 09/19/2002

If Required, Foreign Filing License Granted: 12/11/2003

Projected Publication Date: 06/24/2004

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor integrated circuit

Preliminary Class

330